

COFC



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March 29, 2004

09/822715

Commissioner For Patents
PO BOX 1450
Alexandria, VA 22313-1450

Certificate
APR 08 2004
of Correction

Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 6,710,255
Issued: Mar. 23, 2004
For: **PRINTED CIRCUIT BOARD HAVING BURIED
INTERSIGNAL CAPACITANCE AND METHOD
OF MAKING**

Inventor: Ross et al.

Our File No. 42390.P10576

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Edwin H. Taylor
Reg. No. 25,129

EHT/gx
Enclosures

APR 08 2004

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,710,255
DATED : Mar. 23, 2004
INVENTOR(S) : Ross et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 2, line 64, delete "fonned", insert --formed--.

In column 4, line 60, delete "POB", insert --PCB--.

MAILING ADDRESS OF SENDER
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Los Angeles, CA 90025-1026

PATENT NO. 6,710,255

Certificate of Correction (PTO Form 1050)-Amended

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